

Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11)

EP 1 081 885 A1

(12)

## EUROPEAN PATENT APPLICATION

published in accordance with Art. 158(3) EPC

(43) Date of publication:  
07.03.2001 Bulletin 2001/10

(51) Int. Cl.<sup>7</sup>: H04J 3/04, H04J 3/00,  
H04L 12/56

(21) Application number: 99921201.2

(86) International application number:  
PCT/JP99/02654

(22) Date of filing: 20.05.1999

(87) International publication number:  
WO 99/60736 (25.11.1999 Gazette 1999/47)

(84) Designated Contracting States:  
DE FR GB

(30) Priority: 20.05.1998 JP 13829698

(71) Applicant: Sony Corporation  
Tokyo 141-0001 (JP)

(72) Inventors:  
• NODA, Tatsushi,  
Sony Corp.  
Tokyo 141-0001 (JP)

• ADACHI, Hiroshi,  
Sony Corporation  
Tokyo 141-0001 (JP)  
• YAMAMOTO, Yosuke,  
Sony Corporation  
Tokyo 141-0001 (JP)

(74) Representative:  
Robinson, Nigel Alexander Julian  
D. Young & Co.,  
21 New Fetter Lane  
London EC4A 1DA (GB)

### (54) SEPARATOR AND SEPARATING METHOD, AND RECEIVER AND RECEIVING METHOD

(57) In a separating device and method, plural data streams are received, a corresponding part or all of data streams are selected from the received plural data streams, and the specified data included in each of the selected each data streams are separated and extracted. Moreover, in the signal receiving device and method, the data streams received or the data stream formed of the received transmission signal demodulated are received, a corresponding part or all of data streams are selected from among the received plural input data streams, the specified sequence of data included in each of the data streams selected are separated and extracted, and the prescribed signal processing is applied simultaneously to the part or all of the data separated and extracted.

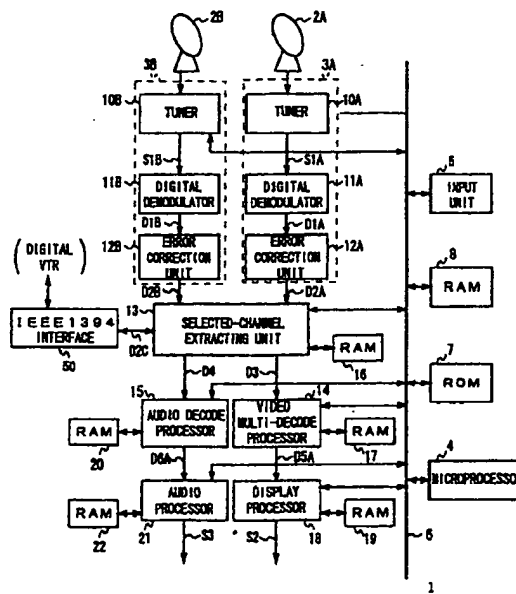


FIG. 1

EP 1 081 885 A1

## Description

### Technical Field

**[0001]** The present invention relates to a separating device and method and a signal receiving device and a signal receiving method, and is suitably applied to a signal receiving device for the digital broadcasting system.

### Background Art

**[0002]** In the conventional digital broadcasting system, the transmitting end conducts the compression coding to the video and audio signals of each program with the MPEG2 (Moving Picture Experts Group Phase 2) standard, and by dividing the various kinds of data to be broadcasted, such as the obtained compression-coded data, control data and the like, per 184-byte and adding TS (transport stream) header of 4-bytes containing sync code (47H) and PID (packet identification, 13-bits) to its head, forms a 18-byte TS packet.

**[0003]** Furthermore, the transmitting end forms a transport stream by multiplexing thus obtained TS packet for plural programs and the TS packet storing program information, QPSK (Quadrature Phase Shift Keying)-modulates these, and sends out the resultant as a digital broadcasting signal.

**[0004]** On the other hand, the receiving end receives and demodulates the digital broadcasting signal, and extracts the TS packets for the program specified by the user included in the obtained transport stream based on the PID. Then, it decodes video and audio data stored in the TS packets to display obtained pictures and to give sound from a speaker.

**[0005]** Accordingly, in the digital broadcasting system, while the transmitting end can multiplex audio and video data for plural programs for transmission, the user can select and watch a desired program from among these programs at the receiving end.

**[0006]** However, in such the signal receiving device of the digital broadcasting system, the device capable of receiving only one sequence of transport stream is used as a demultiplexer to extract the TS packets for a specified program from the transport stream obtained by demodulating the digital broadcasting signal.

**[0007]** For this reason, in such the signal receiving device, for example, an application for displaying pictures of the plural optional programs on the multi-screen upon receiving plural sequences of digital broadcasting signals or an application for displaying pictures of one optional program on the screen upon receiving plural sequences of digital broadcasting signals need the same number of demultiplexers as the digital broadcasting signals. Therefore, this causes the problem in increasing the circuit size.

**[0008]** Accordingly, the conventional signal receiving device has the problem in that it is difficult to improve its operability with minimizing its size, for example, it is

difficult to make an application for displaying efficient without increasing the circuit size.

### Disclosure of Invention

**[0009]** The present invention has been done considering the above point and intends to propose a separating device and its method and a signal receiving device and its method capable of remarkably improving the operability with a simple structure.

**[0010]** To obviate such problems according to the present invention, the separating device provides a selecting means for receiving plural data streams and selecting a corresponding part or all of data streams from among the received plural data streams, and a separating/extracting means for separating/extracting specified sequence of data included in each data stream selected by the selecting means.

**[0011]** As a result, this separating device can cope with input of plural data streams, and for example, it can make an application efficient with a simple structure in the signal receiving device.

**[0012]** Furthermore, according to the present invention, the separating method comprises a first step of receiving plural data streams and of selecting a corresponding part or all of data streams from among the received plural data streams and a second step of separating/extracting the specified data included in each data stream selected at the first step.

**[0013]** As a result, this separating method can cope with input of plural data streams and for example, can easily make an application efficient in the signal receiving device.

**[0014]** Furthermore, according to the present invention, the signal receiving device comprises a separating means for receiving inputted data streams or data streams obtained by modulating inputted transmission signals, for selecting a corresponding part or all of data streams from among the plural data streams received, and for separating/extracting the specified sequence of the data included in each data stream selected, and a signal processing means for applying prescribed signal processing to the part or all of the data separated and extracted by the separating means at the same time.

**[0015]** As a result, this receiving device can cope with input of plural data streams and can make an application efficient with a simple structure.

**[0016]** Furthermore, according to the present invention, the signal receiving method comprises a first step of receiving inputted data streams or data streams obtained by modulating the inputted transmission signals, of selecting a corresponding part or all of the data streams from among the received plural data streams, a second step of separating and extracting the specified sequence of data included in each data stream selected at the first step, and a third step of applying prescribed signal processing to the part or all of the data separated and extracted at the second step at the same time.

**[0017]** As a result, this signal receiving method can cope with input of plural data streams and can easily make an application efficient.

#### Brief Description of Drawings

**[0018]**

Fig. 1 is a block diagram showing the structure of a signal receiving device according to the present invention.

Fig. 2 is a block diagram showing the structure of a selected-channel extracting unit.

Fig. 3 is a flowchart explaining a processing procedure to extract (a) selected channel(s).

Fig. 4 is a schematic diagram explaining extracting processing to select one program from each of three transport streams.

Fig. 5 is a schematic diagram explaining extracting processing to select one program from each of two transport streams.

Fig. 6 is a schematic diagram explaining extracting processing in the case where the PIDs are identical when selecting one program from each of two transport streams.

Fig. 7 is a schematic diagram explaining extracting processing to select two programs from one transport stream.

Fig. 8 is a block diagram showing another embodiment.

#### Best Mode for Carrying Out the Invention

**[0019]** The present invention will be described in detail with reference to the accompanying drawings.

##### (1) Structure of Signal Receiving Device according to the Embodiment of the Present Invention

**[0020]** In Fig. 1, 1 generally shows a signal receiving device of the digital broadcasting system according to the embodiment of the present invention. It has first and the second receiver systems 3A and 3B connected respectively to separate antennas 2A and 2B and can simultaneously receive at least two programs with them under the control of a microprocessor 4. Note that, in Fig. 1, two antennas are provided, however, a signal can be received by one antenna. The signal can be divided into two signals to be supplied to the first and second receiving system. That is, providing two antennas is able to receive digital broadcasting signals from two satellites. On the other hand, providing one antenna is able to select signals which correspond to two transport of a digital broadcasting signal from one satellite.

**[0021]** Channel information of a program desired by a user, inputted with an input unit 5, is supplied to the microprocessor 4 from the input unit 5 through a control

bus 6. That is, the input unit 5 is an infrared ray receiving unit, in which the channel information, from a remote controller not shown is inputted thereto and is supplied to the microprocessor 4 through the control bus 6.

**[0022]** The microprocessor 4 controls tuners 10A and 10B of the first and the second receiver systems 3A and 3B according to the supplied channel information based on a program previously stored in a ROM (Read Only Memory) 7. Note that, a RAM (Random Access Memory) 8 serves as a memory means used for processing of the microprocessor.

**[0023]** As a result, wider the control of the microprocessor 4, the tuners 10A and 10B select the respectively corresponding digital broadcasting signals from among plural digital broadcasting signals received with the antennas 2A and 2B, and transmit these as selected signals S1A and S1B to the digital demodulators 11A and 11B, respectively.

**[0024]** The digital demodulators 11A and 11B conduct the digital demodulation processing to the supplied selected signals S1A and S1B and transmit the resultant demodulated data D1A and D1B to error correction units 12A and 12B, respectively. Then, the error correction units 12A and 12B apply the prescribed error correction processing to the supplied demodulated data D1A and D1B and transmit the resultant transport stream data (hereinafter, referred to as TS data) D2A and D2B to a selected-channel extracting unit 13.

**[0025]** Under the control of microprocessor 4, the selected-channel extracting unit 13 extracts and multiplexes the TS packet data for pictures and the TS packet data for sound of the specified program from among two sequences of TS data D2A and D2B supplied and the, TS data D2C supplied from a digital video tape recorder (VTR) via an IEEE1394 interface 50. Then, the resultant video TS data D3 and audio TS data D4 are transmitted to an video multi-decode processor 14 and an audio decode processor 15, respectively, or are stored in a RAM 16. Note that, the video TS data D3 is a signal generated by multiplexing TS data for one or plural program(s). In addition, the audio TS data D4 is TS data for one program (for example, a program displayed on a main screen of programs displayed on the multi-screen).

**[0026]** Under the control of the microprocessor 4, the video multi-decode processor 14 simultaneously decodes the video TS packet data for one or plural program(s) included in the supplied video TS data D3 with a RAM 17 and transmits the resultant video data D5 for one or plural program(s) to a display processor 18.

**[0027]** The display processor 18, under the control of the microprocessor 4, executes the prescribed signal processing with a RAM 19 for multi-screen displaying, and transmits the obtained display signal S2 to a monitor not shown to display pictures based on the display signal S2 on the multi-screen, in the case where pictures of plural programs are included in the video data D5. Note that, in the case where pictures of only one

program are included in the video data D5, the prescribed signal processing is executed so that the picture can be display on the screen all over as a conventional device.

**[0028]** On the other hand, under the control of the microprocessor 4, audio decode processor 15 decodes the supplied audio TS packet data D4 with a RAM 20 and then, transmits the resultant audio data D6 to an audio processor 21.

**[0029]** Then, under the control of the microprocessor 4, the audio processor 21 performs the digital-to-analog conversion on the supplied audio data and outputs it to the speaker not shown, or converts the supplied audio data into an optical digital sound output signal to output.

**[0030]** In this way, this signal receiving device 1 can display pictures of plural programs, specified from two different sequences of digital broadcasting signals and the TS data obtained via the IEEE1394 interface 50 by the user, on the multi-screen by the such a method of dividing the screen, and at the same time, can give sound of any one of the programs from the speaker.

**[0031]** On the other hand, when the user manipulates the remote commander to specify one program and the input unit 5 receives a manipulated signal transmitted from the remote commander, the microprocessor 4 controls only one of the tuners 4A and 4B of the first and second receiver systems 3A and 3B and the IEEE interface 50, in order to select a digital broadcasting signal of the tuner 10A or 10B or a digital broadcasting signal of the IEEE1394 interface 50 (a digital reproduction signal from the digital VTR).

**[0032]** As a result, the selected digital broadcasting signal of the tuner 4A or 4B of the first or the second receiver system 3A or 3B is subjected to the demodulating processing and the error correction by the following digital demodulator 11A or 11B and error correction unit 12A or 12B to restore the transport stream. Then, the transport stream is supplied to the selected-channel extracting unit 13 as TS data D2A or D2B. On the other hand, in the case of specifying the digital signal obtained via the IEEE1394 interface 50, the digital signal is subjected to the error correction processing by an error correction unit (not shown) provided at the output stage of the IEEE1394 interface 50, to obtain TS data D2C in the form of a transport stream. Then, the TS data D2C is supplied to the selected-channel extracting unit 13.

**[0033]** Then, the selected-channel extracting unit 13 separates video TS packets and audio TS packets for the program specified by the user from the TS data D2A or D2B or the TS data D2C obtained via the IEEE1394 interface 50 under the control of the microprocessor 4. The obtained video TS data D3 and audio TS data D4 are supplied to the video multi-decode processor 14 and audio decode processor 15 respectively.

**[0034]** Then, the video TS data D3, after being decoded by the video multi-decode processor 14 under

the control of the microprocessor 4, is converted into an analog signal by the display processor 18 and is sent to the monitor as a display signal S2. As a result, the pictures of only one program specified by the user is displayed on the monitor.

**[0035]** Moreover, the audio TS data D4, after being decoded by the audio decode processor 15 under the control of the microprocessor 4, is converted to an analog signal by the audio processor 21 and transmitted to the speaker as an audio signal S3. As a result, the sound of the program specified by the user is given from the speaker.

**[0036]** In this way, this signal receiving device 1 can select only one sequence of TS data D2A or D2B from among two sequences of TS data D2A and D2B to be given to the selected-channel extracting unit 13 from the first and the second receiver systems 3A and 3B and the TS data D2C obtained via the IEEE1394 interface 50, to display the pictures and to give sound with respect to the specified program.

## (2) Structure of Selected-channel Extracting unit 13

**[0037]** In practice, the selected-channel extracting unit 13 is constructed as shown in Fig. 2, in which the TS data D2A and D2B to be supplied respectively from the error correction units 12A and 12B of the first and second receiver systems 3A and 3B are supplied into the corresponding first or second sync code detecting/interchanging units 31A and 31B of the TS selector/multiplexer 30.

**[0038]** The first and second sync code detecting/interchanging units 31A and 31B detect sync code stored in the TS header of each TS packet included in the respectively supplied TS data D2A and D2B, and transmit a sync code detection signal S10 to a timing controller 32 whenever detecting a sync code.

**[0039]** In addition to this, the first and the second sync code detecting/interchanging units 31A and 31B rewrites the sync code of each TS packet to an ID code for internal processing to be given from the microprocessor 4 through the control bus 6, a control interface 33 and the timing controller 32 and changes identical PIDs (Packet Identification) so that the PIDs of TS packets of each program specified can be identified even if the PIDs are the same. Then, these TS packet data D10A and D10B are sequentially stored in the corresponding first and the second FIFOS (First-in First-out) 33A and 33B.

**[0040]** On the other hand, the timing controller 32, based on the sync code detection signals S10 supplied from the first and second sync code detecting/interchanging units 31A and 31B and the control signal supplied from the microprocessor 4 through the control interface 33, reads out, for example, data in a TS packet unit (in a 188-byte unit) from the first and second FIFOs 33A and 33B alternately with a double clock frequency, multiplexes the first and second TS data D2A and D2B

with a multiplexer 51, and transmits the resultant stream data to a TS separator 34 via the multiplexer 51 as selected/multiplexed data D11, in the case where each of plural programs specified by the user is included in each of two transport streams.

**[0041]** On the other hand, the third sync code detecting/interchanging unit 31C detects a sync code stored in the TS header of each TS packet included in the supplied TS data D2C, and transmits the sync code detection signal S10 to the timing controller 32 whenever detecting a sync code.

**[0042]** Further, even when a TS packet included in the inputted TS data D2C has the same PID as a TS packet for each program specified by the first and second sync code detecting/interchanging unit 31A and 31B, the third sync code detecting/interchanging unit 31C rewrites the sync code of each TS packet to the identification code for internal processing supplied from the microprocessor and interchanges the identical PIDs (Packet Identification) so that the PIDs can be identified. Thereafter, it sequentially stores the TS packet data D10C to the corresponding third FIFO (First-In First-Out) 33.

**[0043]** The timing controller 32, based on the sync code detection signal S10 supplied from the third sync code detecting/interchanging unit 31C and the control signal supplied from the microprocessor 4 through the control interface 33, reads, for example, data in the TS packet unit (in a 188-byte unit) from the first, second and third FIFOs 33A, 33B and 33C alternately with a three-fold clock frequency, multiplexes the first, second and third TS data D2A, D2B and D2C with the multiplexer 51, and transmits the resultant stream data to the TS separator 34 as selected/multiplexed data D11, in the case where each of plural programs specified by the user are included in each of three transport streams.

**[0044]** In this case, firstly, the TS separator 34 extracts the TS packet having the program information included in the digital broadcasting signal selected at the first and the second receiver systems 3A and 3B from the selected/multiplexed data D11 and the TS packet having the program information included in the digital signal selected from the TS data D2C obtained through the IEEE1394 interface 50, and transmits these to the microprocessor 4 through the control interface 33 and the control bus 6 in order.

**[0045]** Then, the microprocessor 4 obtains the PID of each video and audio TS packet for the program specified based on the program information of each digital broadcasting signal to be supplied from the TS separator 34 and transmits it to the TS separator 34 through the control bus 6 and the control interface 33 in order.

**[0046]** Thus, the TS separator 34 extracts TS packets of such as video, audio, control data and data signal for each program specified from among the selected/multiplexed data D11 based on the PID to be given from the microprocessor 4, and transmits the control information and data signal for each program to the

microprocessor 4 via the control interface 33 and the same time, transmits the video and audio TS packet data for each program to a PID interchange and sync code conversion unit 35 or stores them in the RAM 16 through the RAM interface 36, as the video TS packet data D12A and audio TS packet data D12B.

**[0047]** In this connection, the TS separator 34 can select TS packets corresponding to the program selected by the user from among the TS data D2A and D3A received by the first and second receiver system 3A and 3B to generate new TS data D2C, and can record it on the digital VTR through the IEEE1394 interface 50. In this case, the TS separator 34 is to output the relation between the current PID and the former one to the digital VTR through the IEEE1394 as interchange information when the TS packet having the PID interchanged exists in the TS data D2C.

**[0048]** Under the control of the microprocessor 4, the PID interchange and sync code conversion unit 35 rewrites the ID code and PID attached by the first, second, and third sync code detecting/interchanging units 31A, 31B and 31C to original synchronization codes with respect to each video and audio TS packet of the supplied video TS packet data D12A and audio TS packet data D12B.

**[0049]** Furthermore, in the case where the PIDs of the video and audio TS packets for plural programs specified by the user are identical, the PID interchange and sync code conversion unit 35 transmits information to specify the channel numbers of TS packets having identical PIDs to the video multi-decode processor 14 or to the audio decode processor 15 through a control line CONT.

**[0050]** On the other hand, in the case where only one program is specified by the user with the input unit 5, under the control of the microprocessor 4, the timing controller 32 reads out the data in the TS packet unit (in a 188-byte unit) with a normal speed only from the corresponding first, second or third FIFO 33A, 33B or 33C based on the sync code detection signal S10 to be given from the corresponding first, second or third sync code detecting/interchanging unit 31A, 31B, and transmits this to the TS separator 34 as the selected/multiplexed data D11.

**[0051]** At this point, the TS separator 34 extracts TS packets having program information from the supplied selected/multiplexed data D11, and transmits these to the microprocessor 4 through the control interface 33 and the control bus 6 in order.

**[0052]** Then, the microprocessor 4 obtains the PID of each video and audio TS packet for programs specified by the user based on the program information to be supplied from the TS separator 34, and outputs this to the TS separator 34 through the control bus 6 and the control interface 33 in order.

**[0053]** Thus, the TS separator 34 extracts each TS packet of such as picture, audio, control data and data signal for the specified program from the selected/multi-

plexed data D11 based on the PID supplied from the microprocessor 4, and transmits these control information and data signal to the microprocessor 4 through the control interface 33 and the control bus 6 in order and the same time, transmits data of each TS packet for picture and sound to the PID interchange and sync code conversion unit 35 or stores them in the RAM 16 through the RAM interface 36, as the video TS packet data D12A and audio TS packet data D12B.

[0054] In the case where only one program is specified by the user, since it is impossible that PIDs are identical, the PID interchange and sync code conversion unit 35 does not interchange a PID. Therefore, the PID interchange and sync code conversion unit 35 transmits the supplied video TS packet data D12A to the video multi-decode processor 14 as the video TS packet stream data D3 as it is and transmits the supplied audio TS packet data D12B to the audio decode processor 15 as the audio TS packet stream data D4 as it is, under the control of the microprocessor 4.

[0055] In this way, the selected-channel extracting unit 13 can select and output the video and audio TS packets for plural or one program(s) specified from the TS data D2A and D2B supplied from the error correcting units 12A and 12B in the first and second receiver systems 3A and 3B respectively and the TS data D2C supplied through the IEEE1394 interface 50.

[0056] Here, Fig. 3 shows a processing procedure of extracting a selected channel in the selected-channel extracting unit 13. The selected-channel extracting unit 13 controlled by the microprocessor 4 enters the processing procedure from step SP10, and then judges the number of programs specified by the user at following step SP11. When one program is specified, the selected-channel extracting unit 13 proceeds to step SP13 to perform a selection and a decoding error correction by controlling one receiver system (out of the first and second receiver systems 3A and 3B and the third receiver system through the IEEE1394 interface 50). Note that, the IEEE1394 interface 50 also performs the selection and the decoding error correction as required.

[0057] Then, the selected-channel extracting unit 13 outputs the resultant video TS packet stream data D3 and the audio TS packet stream data D4 to the video multi-decoder 14 and the audio decoder 15 at step SP16, and terminate the processing procedure at step SP20.

[0058] On the other hand, in the case where plural programs are specified at step SP11, the selected-channel extracting unit 13 proceeds to step SP12. In the case where the specified programs are included only in the TS data obtained through one of three receiver systems (the first and second receiver system 3A and 3B and the third receiver system through the IEEE1394 interface 50), the selected-channel extracting unit 13 obtains a negative result at step SP12. Then, it proceeds to step SP14 to perform the selection and the

decoding error correction by controlling one receiver system (out of the first and second receiver systems 3A and 3B and the third receiver system through the IEEE1394 interface 50). In this connection, the IEEE1394 also performs the selection and the error correction as required.

[0059] After that, the selected-channel extracting unit 13 outputs the resultant video TS packet stream data D3 and the audio TS packet stream data D4 to the video multi-decoder 14 and the audio decoder 15 at step SP16, and terminate the processing procedure at step SP20.

[0060] On the other hand, when a positive result is obtained at step SP12, that is, when the programs specified by the user are included in TS data obtained by two or more receiver systems (two or more out of the first and second receiver systems 3A and 3B and the third receiver system through the IEEE1394 interface 50), the selected-channel extracting unit 13 obtains the positive result at step SP12. Then, it proceeds to step SP15 to perform the selection and the error correction by controlling two or more receiver systems (two or more out of the first and second receiver systems and the third receiver system through the IEEE1394 interface 50) having the TS data specified.

[0061] After that, the selected-channel extracting unit 13 proceeds the following step SP17 to judge whether or not there are identical PIDs in the selected TS packets. When the identical PIDs exist, it proceeds to following step SP18 to interchange the PIDs, on the other hand, proceeds to step SP19 when identical PIDs do not exist.

[0062] The selected-channel extracting unit 13 outputs the video TS packet stream data D3 and the audio TS packet stream data D4 to the video multi-decoder 14 and the audio decoder 15 at step SP19, and then terminates the processing procedure at step SP20.

### (3) Operation and Effects of the Embodiment

[0063] According to the above-mentioned structure, in the case of selecting one program from each of digital broadcasting signals and the TS data reproduced from the digital VTR D2A (Fig. 4(A)), D2B (Fig. 4(B)) and D2C (Fig. 4(C)) respectively corresponding to the specified programs in the first and second receiver systems 3A and 3B and the third receiver system for receiving TS data through the IEEE1394 interface 50 as shown in Fig. 4, the receiving device 1 reads three TS data D2A, D2B and D2C from the FIFOs with the timing controller 32 with a threefold clock. And these data becomes data compressed in a temporal axis direction as shown in Fig. 4(D).

[0064] In the TS separator 34, by extracting only TS packets having video data for the selected program, extracts the TS packets (PID = 3) for the channel 2 included in the first TS data D2A, the TS packets (PID = 15) for the channel 6 included in the second TS data

D2B, and the TS packets (PID = 23) for the channel 8 included in the third TS data D2C, as shown in Fig. 4(E).

[0065] In addition, at this time, the TS separator 34 extracts the TS packets having one piece of audio data for the specified program, to extract the TS packets (PID = 4) for the channel 2 included in the first TS data D2A, for example, as shown in Fig. 4(F).

[0066] In this way, one program is selected from each of 3 piece of TS data D2A, D2B, and D3C received by the first to third receiver systems, and the pictures of the total three programs and sound of one program out of three programs can be displayed on the display.

[0067] In this case, for example, the screen of the display formed of the CRT (Cathode Ray Tube) displays the picture of each program in one area out of three areas which the screen is divided into, and the sound of one program out of the displayed programs is given from the speaker provided in the display unit.

[0068] On the contrary, in the case where the user selects programs each included in the TS data D2A or D2B obtained through the two receiver systems (for example, the first receiver system 3A and the second receiver system 3B) out of three receiver systems, the two pieces of TS data D2A (Fig. 5(A)) and D2B (Fig. 5(B)) are read out from the FIFOs with a double clock by the timing controller 32 as shown in Fig. 5. These data are multiplexed as data compressed in the temporal axis direction as shown in Fig. 5(C) and then, are supplied to the TS separator 34.

[0069] Then, the TS separator 34, by extracting only the TS packets having video data for the selected programs, extracts the TS packets (PID = 3) for the channel 2 included in the first TS data D2A and the TS packets (PID = 15) for the channel 6 included in the second TS data D2B, for example, as shown in Fig. 5(D).

[0070] Further, at this time, the TS separator 34, by extracting the TS packets having audio data for one of the selected programs, extracts the TS packets (PID = 4) for the channel 2 included in the first TS data D2A, for example, as shown in Fig. 5(E).

[0071] In this way, one program can be selected from each of two pieces of TS data D2A and D2B received by the first and second receiver system, and the pictures of the total two programs and the sound of one program out of two can be displayed at the display.

[0072] In this case, the picture of each program is displayed in one area of two areas which the screen is divided into, and the sound of one program out of the displayed programs can be given from the speaker provided at the display unit.

[0073] Furthermore, on the contrary, when the user selects a program from each of TS data D2A and D2B obtained by the two receiver system (for example, the first receiver system 3A and the second receiver system 3B) out of three receiver systems, if TS packets having the identical PIDs exist in the selected two TS data D2A and D2B, the two TS data D2A (Fig. 6(A)) and D2B (Fig. 6(B)) are read out from the FIFOs with the double clock

by the timing controller 32 and then are multiplexed as data compressed in the temporal axis direction as shown in Fig. 6(C). In this case, the sync code detecting/interchanging unit 31B has interchanged the PID of the TS packet in the second TS data D2B, which was the same as that in the first TS data D2A, so that the PIDs of the TS packet for the channel 4 in the second TS data D2B has been changed from "PID = 3" to "PID = a", as shown in Fig. 6(C). In this way, the TS data D2B having the changed PID of the TS packet and the TS data D2A having TS packet having the original PID are multiplexed by the multiplexer 51, to obtain a sequence of TS packets having PIDs different from each other, as shown in Fig. 6(C). This sequence of TS packets is supplied to the TS separator 34.

[0074] Then, the TS separator 34 extracts only the TS packets having video data for the selected program to extract the TS packets (PID = 3) for the channel 2 included in the first TS data D2A and the TS packet (PID = 7(e)) for the channel 6 included in the second TS data D2B, for example, as shown in Fig. 6(D).

[0075] In addition, at this time, the TS separator 34 extracts the TS packet having audio data for one program out of the selected ones to extract the TS packets (PID = 8(f)) for the channel 6 included in the first TS data D2A, for example, as shown in Fig. 6(E).

[0076] In this way, one program can be selected from each of two pieces of TS data D2A and D2B received by the first and second receiver systems, the pictures of the total two programs and the sound of one program out of them can be displayed at the display.

[0077] In this case, the picture of each program are displayed in one area out of two areas which the screen is divided into, and the sound of one program out of them being displayed is given from the speaker provided at the display.

[0078] Furthermore, on the contrary, in the case where the user selects two programs included in the TS data D2A obtained by one receiver system (for example, the first receiver system 3A) of three receiver systems, as shown in Fig. 7, one piece of TS data D2A (Fig. 7(A)) is read out from the FIFO with a normal clock by the timing controller 32, and is supplied to the TS separator 34 as data non-compressed in the temporal axis direction as shown in Fig. 7(B).

[0079] Then, the TS separator 34 extracts only the TS packets having video data for the selected programs to extract the TS packets (PID = 3) for the channel 2 included in the first TS data D2A and the TS packets (PID = 5) for the channel 3 included in the first TS data D2A as shown in Fig. 7(C), for example.

[0080] In addition, at this time, the TS separator 34 extracts the TS packets having audio data for one program out of the selected programs to extract the TS packets (PID = 6) for the channel 3 included in the first TS data D2A, for example, as shown in Fig. 7(D).

[0081] In this way, two programs are selected from the one piece of TS data D2A received by the first

receiver system, the picture of the total two programs and the sound of one programs out of two can be displayed at the display unit.

**[0082]** In this case, the picture of each program is displayed in one area of two areas which the screen is divided into and the sound of one program out of the programs being displayed are given from the speaker provided at the display unit.

**[0083]** In this way, in the case where the user specifies plural programs, the receiving device 1 selects a digital broadcasting signal or the TS data reproduced from the digital VTR corresponding to the specified program in the first and second receiver systems 3A and 3B and the third receiver system for receiving TS data through the IEEE1394 interface 50, sequentially performs the demodulating and the error correction processing with the corresponding digital demodulator and the error correction unit, and then separates and extracts video TS packets for one or more program(s) and audio TS packets for one program specified by the obtained first, second and third TS data D2A, D2B and D2C.

**[0084]** After that, the obtained video TS packet data D3 for plural programs are simultaneously decoded by the video multi-decode processor 14 and the audio TS packet data D4 for one program is decoded by the audio decode processor 15.

**[0085]** In addition, the pictures for plural programs specified based on the obtained video data D5A and D5B are subjected to the signal processing for display on multi-screen by the display processor 18, and the sound of the specified program is selected by the audio processor 21.

**[0086]** Thus, the receiving device 1 can receive plural programs from the plural digital broadcasting signals and can display them on multi-screen, thereby making it possible to have a high function with respect to display on a screen.

**[0087]** Further, in the receiving device 1, the selected-channel extracting unit 13 can separate the TS packets for programs specified by the user from the TS data D2A, D2B, and D2C correspondingly to the inputs of plurality of TS data D2A, D2B and D2C, so that, for example, it is not necessary to provide plural demultiplexers for extracting TS packets for a program specified by the transport stream, thereby making it possible to construct it simply.

**[0088]** According to the above-mentioned structure, the TS data D2A, D2B, D2C outputted from the first, second, third receiver systems are selected by the selected-channel extracting unit 13 as required, the TS packets having various kinds of data such as pictures and sound for a specified program included in the selected TS data D2A, D2B and D2C are separated and extracted, thereby making it possible to make an application efficient with a simple structure. Thus, it is possible to significantly improve its function with a simple structure.

#### (4) Other Embodiments

**[0089]** Note that, in the aforementioned embodiment, two antennas are provided. However, as shown in Fig. 8, a signal from one antenna 2C can be divided into two signals by a divider 40, and be supplied to the first and second receiver systems. That is, in the case of providing two antennas, digital broadcasting signals from two satellites can be received, and in the case of providing only one antenna, a signal corresponding to two transponders in the digital broadcasting signal from one satellite can be selected.

**[0090]** Further, in the embodiment described above, the selected-channel extracting unit 13 is constructed as a separating device according to the present invention as shown in Fig. 2. However, the present invention is not only limited to this but various kinds of structures can be applied as the structure of the selected-channel extracting unit 13 as long as the data of the specified sequences can be separated and extracted from the data stream generated by multiplexing plural sequences of data in a prescribed unit.

**[0091]** In this case, in the embodiment described above, the TS selector/multiplexer 30 is comprised of the first, second and third sync code detecting/interchanging units 31A, 31B and 31C, the FIFOs 33A, 33B, and 33C, and the timing controller 32 as the selecting means for receiving plural data streams and selecting the corresponding part or all of the data streams from among the received plural data streams, as shown in Fig. 2. However, the present invention is not limited thereto and other various kinds of structures can be applied.

**[0092]** Furthermore, in the embodiment described above, the TS separator 34 serving as the separating/extracting means for separating and extracting data (video data and audio data) of the sequences specified by the user (pictures and sound of the specified program) included in the TS data D2A, D2B and D2C is placed at the stage following the FIFOs 33A, 33B and 33C (and the timing controller 32) serving as the multiplexing means for multiplexing these data. However, the present invention is not only limited to this but also the TS separator 34 can be placed at the stage preceding the FIFOs 33A, 33B and 33C, and the same effects as the above embodiment can be obtained.

**[0093]** Furthermore, in the embodiment described above, the selected-channel extracting unit 13 multiplexes and outputs video and audio data for the programs specified by the user in the form of a TS packet. However, the present invention is not only limited to this but also video and audio data for the programs specified by the user can be multiplexed and outputted in the form of a signal, such as ES (Elementary Stream) and PES (Packetized Elementary Stream). In this case, the signal receiving device can be so constructed that the selected-channel extracting unit 13 outputs a control signal so as to make the video multi-decode processor



14 and the audio decode processor 15 decide what program the video TS data D3 and audio TS data D4 to be supplied at that moment belong to.

**[0094]** Furthermore, in the embodiment described above, the selected-channel extracting unit 13 is constructed so as to cope with the inputs of three transport streams. However, the present invention is not only limited to this but also, the selected-channel extracting unit 13 can cope with four or more transport streams as long as it is constructed so as to receive plural data streams, to select corresponding part or all data streams from among the received plural data streams, and to separate and extract data of a specified sequence included in the selected data streams.

**[0095]** Furthermore, in the embodiment described above, identical PIDs of TS packets are interchanged. However, the present invention is not limited thereto but also, for example, with respect to a TS packet having another identical PID, a flag to specify the channel number can be added to its header to output the TS packet from the selected-channel extracting unit 13 through a control line CONT.

**[0096]** Furthermore, in the embodiment described above, the receiving device according to the present invention is applied to a receiving device in a digital broadcasting system. However, the present invention is not limited thereto but also can be applied to other various types of receiving device such as a receiving device for the cable television broadcasting. In this case, a program of a different broadcasting system can be extracted by making a receiver system cope with the different broadcasting system (for example, the CS (Communications Satellite) broadcasting for distributing programs via a communications satellite, the BS (Broadcasting Satellite) broadcasting for distributing program via a broadcasting satellite, the cable television broadcasting, or the digital broadcasting for distributing with a ground wave).

#### Industrial Applicability

**[0097]** The present invention can be utilized for the case of simultaneously displaying programs, distributed with plural broadcasting system such as the CS broadcasting, the BS broadcasting, or the cable television broadcasting, on one monitor.

#### Reference Numerals

**[0098]** 1... .. signal receiving device, 3A, 3B... .. signal receiver system, 4... .. microprocessor, 13... .. selecting/multiplexing/separating unit, 14... .. video multi-decode processor, 18... .. display processor, 15... .. audio multi-decode processor, 21... .. audio processor, 30... .. TS selector/multiplexer, 31A, 31B... .. sync code detecting/interchanging unit, 32... .. timing controller, 33A, 33B... .. FIFO, 34... .. TS separator, 35... .. PID interchange and sync code conversion unit, D2A,

D2B... .. TS data, D3... .. video TS data, D4... .. audio TS data, D11... .. selected/multiplexed data.

#### Claims

1. A separating device for separating/extracting a specified sequence of data from a data stream formed of plural sequences of data multiplexed per the fixed unit, comprising:

selecting means for receiving a plural of said data streams and for selecting a corresponding part or all of said data stream from among said plural data streams received, and separating/extracting means for separating/extracting said specified sequence of data included in each of said data streams selected by said selecting means.

2. The separating device according to Claim 1, comprising

multiplexing means is provided at the preceding stage or at the following stage of said separating/extracting means for multiplexing said data streams selected by said selecting means or said specified data to be sent out from said separating means.

3. The separating device according to Claim 1, wherein:

said data stream is formed of said plural sequences of data packetized and multiplexed per the prescribed unit; and said separating/extracting means outputs said specified data separated and extracted, in the form of packetized data.

4. A separating method for separating/extracting said specified data from a data stream formed of plural data multiplexed per the prescribed unit, comprising:

a first step of receiving said plural data streams and selecting a corresponding part or all of said data streams from among said plural data streams received; and a second step of separating/extracting said specified data included in each of said data streams selected at said first step.

5. The separating method according to Claim 4, comprising

a multiplexing step is conducted before or after said second step for multiplexing each of said data streams selected at said first step or said

specified data to be sent out from said separating means.

6. The separating method according to Claim 4, wherein:

said data stream is formed of said plural sequences of data packetized and multiplexed per the prescribed unit; and  
said second step outputs said specified data separated and extracted in the form of packetized data.

7. A signal receiving device for receiving a data stream formed of plural sequences of data multiplexed per the fixed unit or for receiving a transmission signal formed of said data stream modulated, comprising:

separating means for inputting said data stream received or said data stream formed of the received transmission signal demodulated, for selecting a corresponding part or all of said data streams from among said input plural data streams, and for separating/extracting the specified sequence of data included in each of said selected data streams; and  
signal processing means for applying the prescribed signal processing simultaneously to the part or all of each of said data separated/extracted by said separating means.

8. The signal receiving device according to Claim 7, comprising

multiplexing means provided at the preceding stage or following stage of said separating/extracting means for multiplexing each of said data streams selected by said selecting means or said specified data to be sent out from said separating means.

9. The signal receiving device according to Claim 7, wherein:

said data stream is formed of said plural sequences of data packetized and multiplexed per the prescribed unit; and  
said separating means outputs said specified data separated/extracted in the form of packetized data.

10. A signal receiving method of receiving data stream formed of plural sequences of data multiplexed per the prescribed unit or a transmission signal formed of said data stream demodulated, comprising:

a first step of inputting said data stream

received or said data stream formed of said received transmission signal demodulated, and of selecting a corresponding part or all of said data streams from among said plural data streams;

a second step of separating/extracting the specified data included in each of said data streams selected at the first step; and  
a third step of applying the prescribed signal processing simultaneously to the part or all of said data separated/extracted at the second step.

11. The signal receiving method according to Claim 10, comprising

a multiplexing step to be conducted before or after the second step for multiplexing each of said data streams selected at the first step or said specified data to be sent out from said separating means.

12. The signal receiving device according to Claim 10, wherein:

said data stream is formed of plural sequences of data packetized and multiplexed per the prescribed unit; and  
said second step outputs said specified data separated and extracted in the form of packetized data.

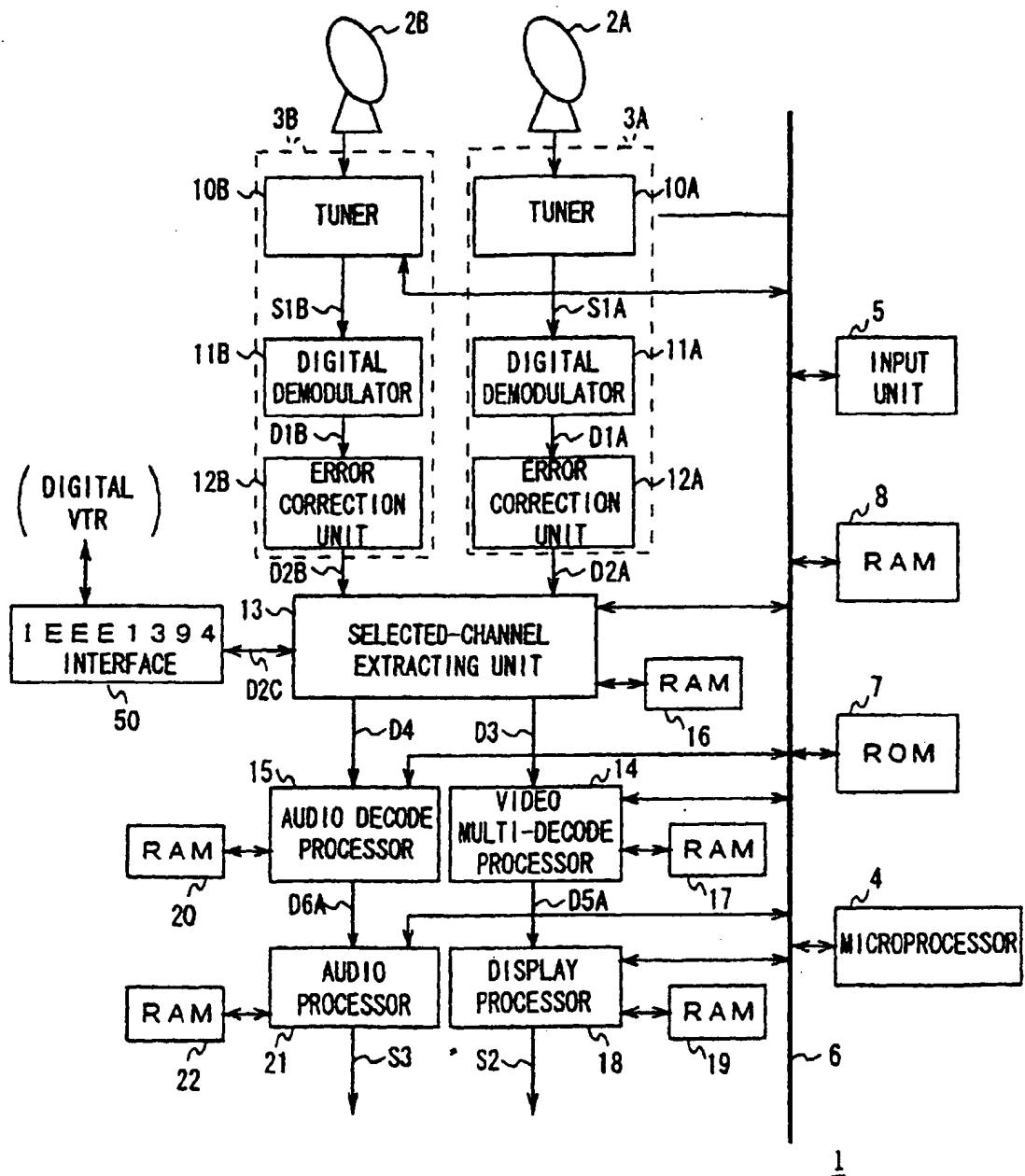


FIG. 1

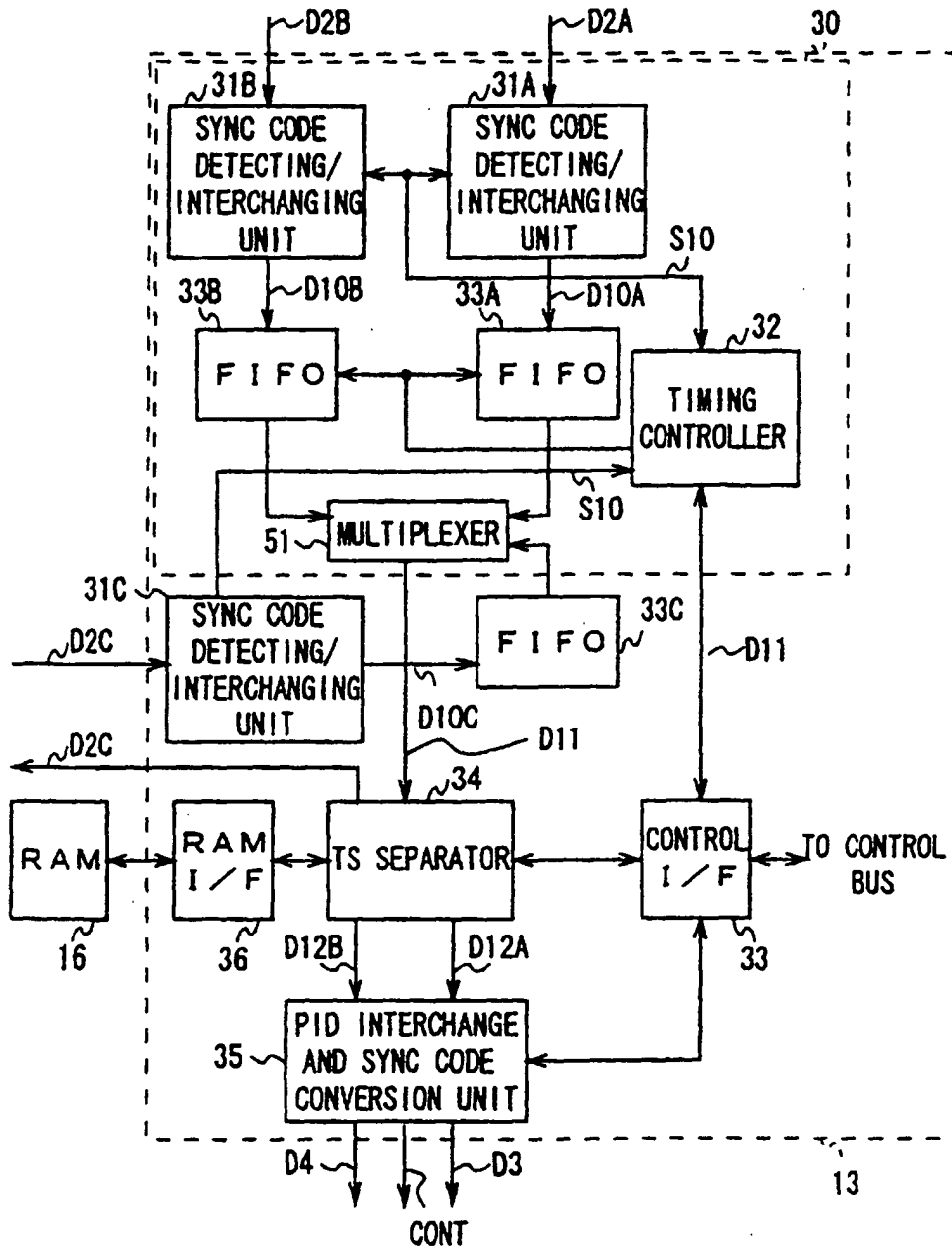


FIG. 2

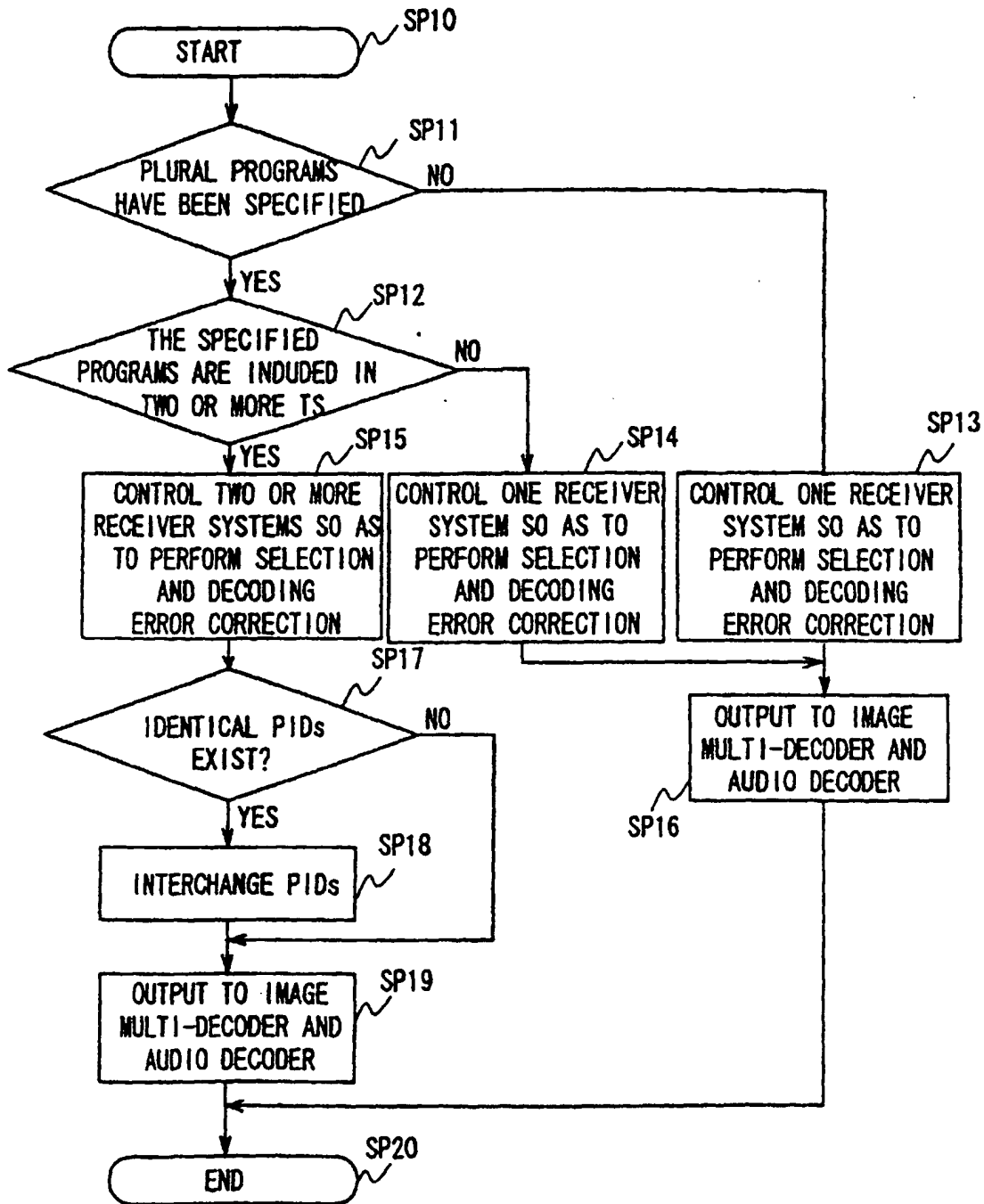


FIG. 3

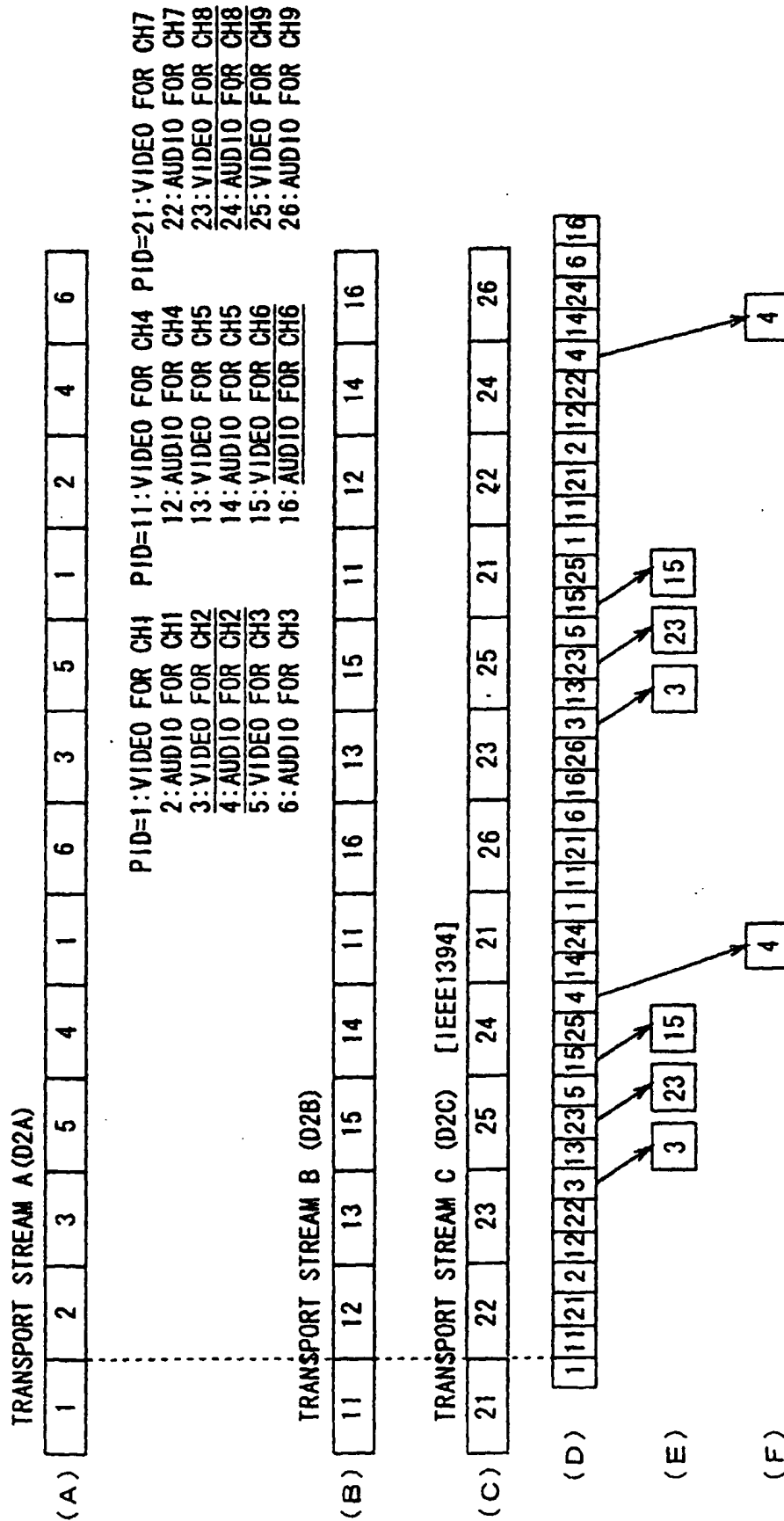


FIG. 4

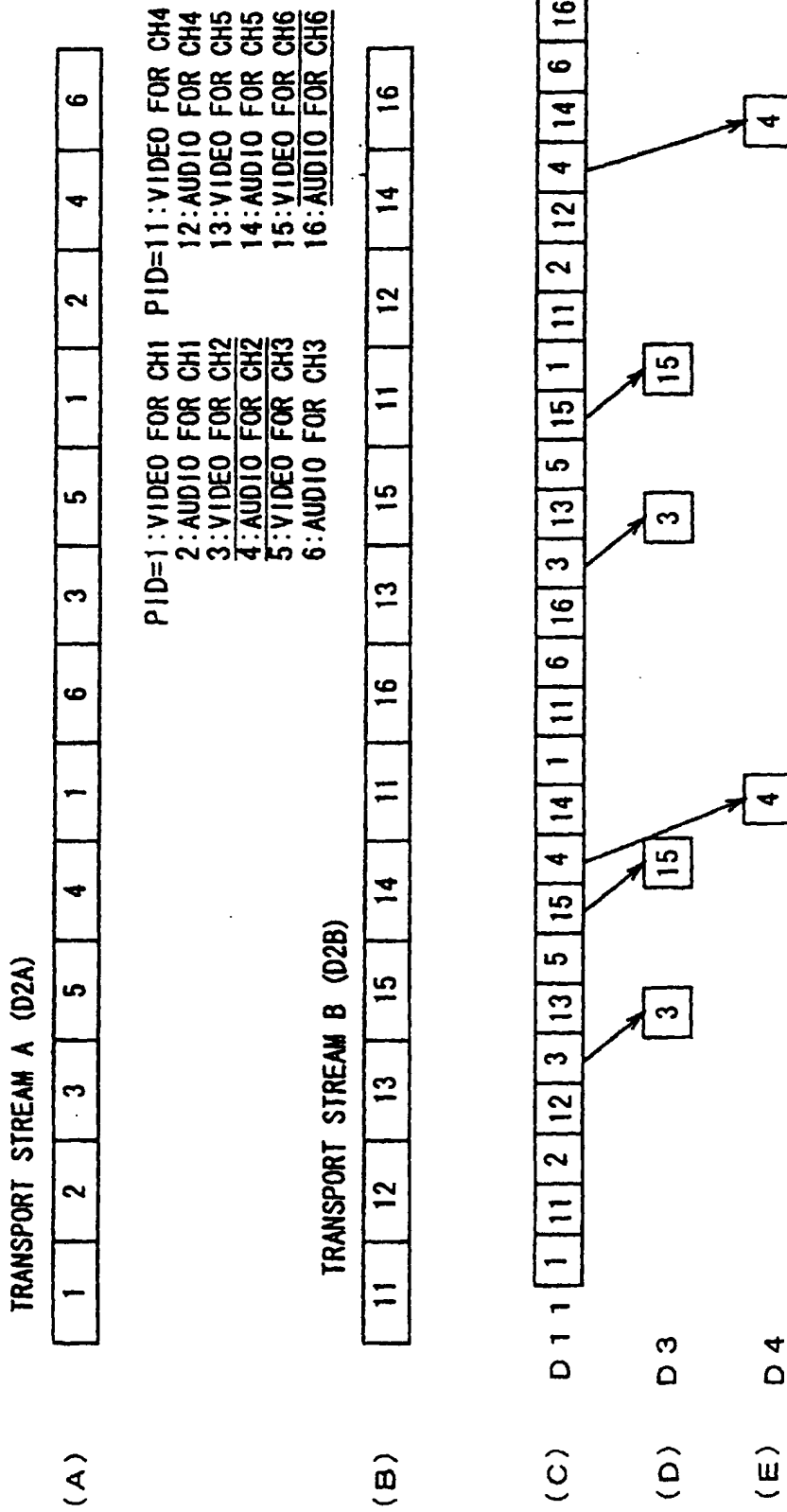


FIG. 5

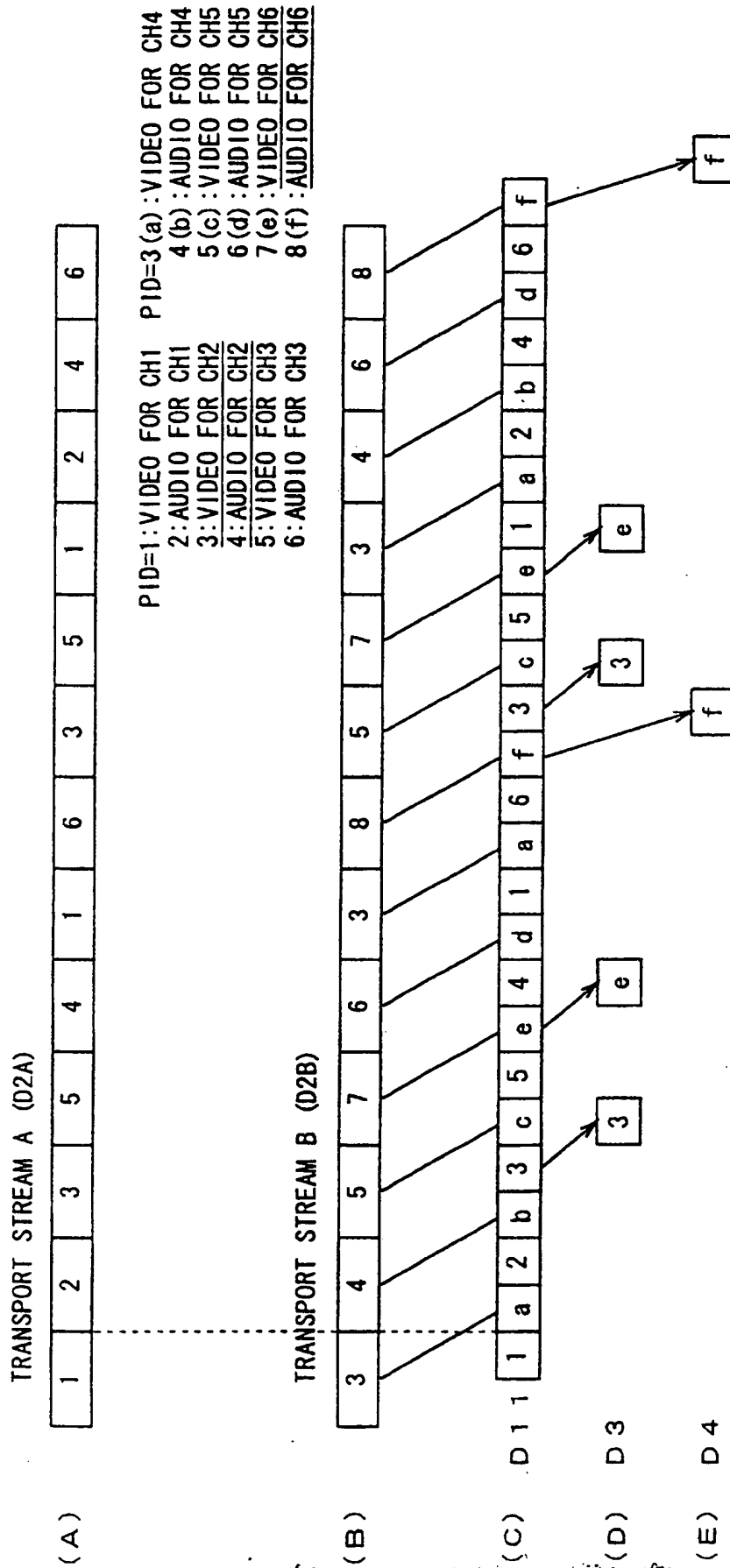


FIG. 6



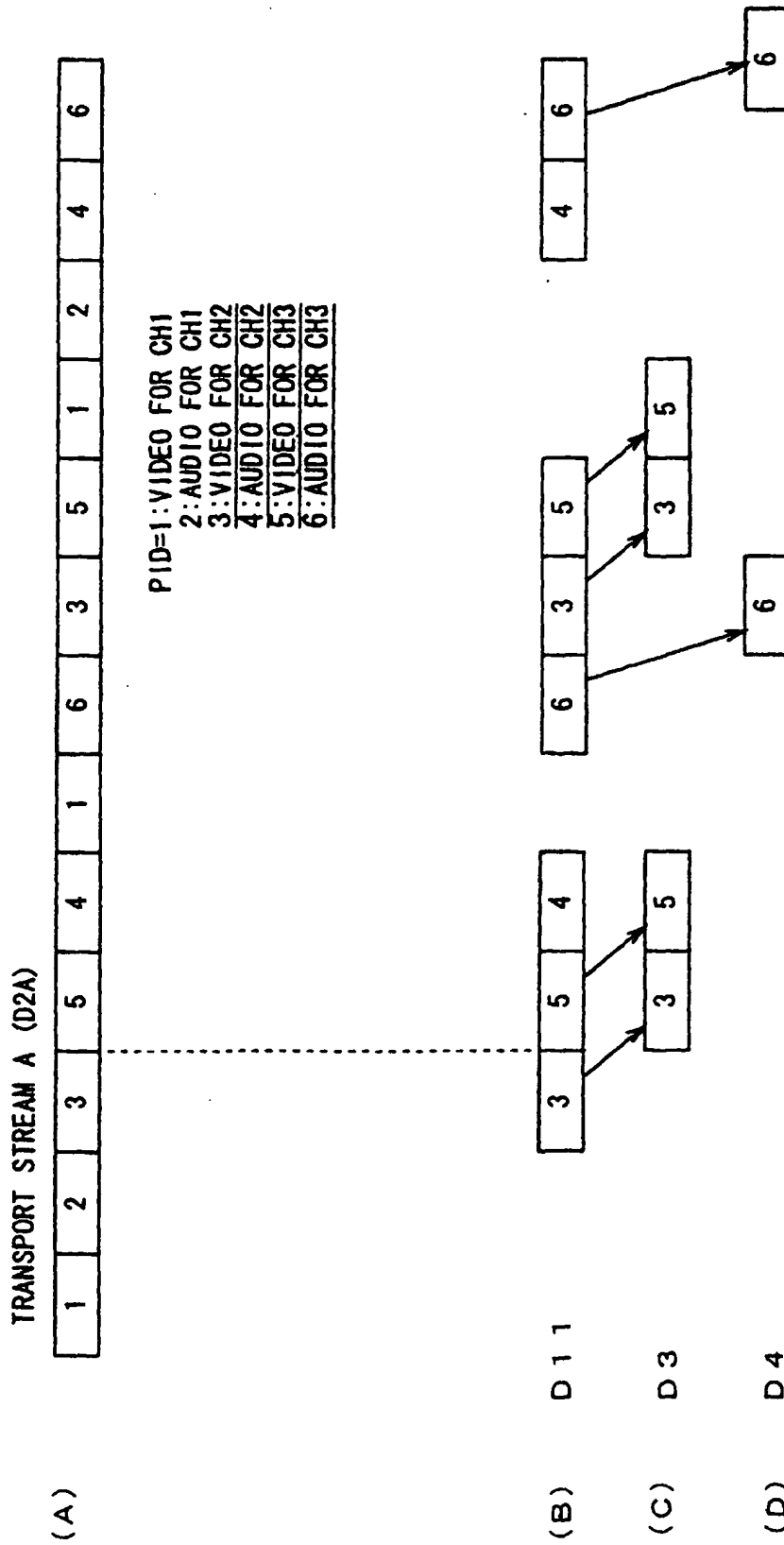


FIG. 7

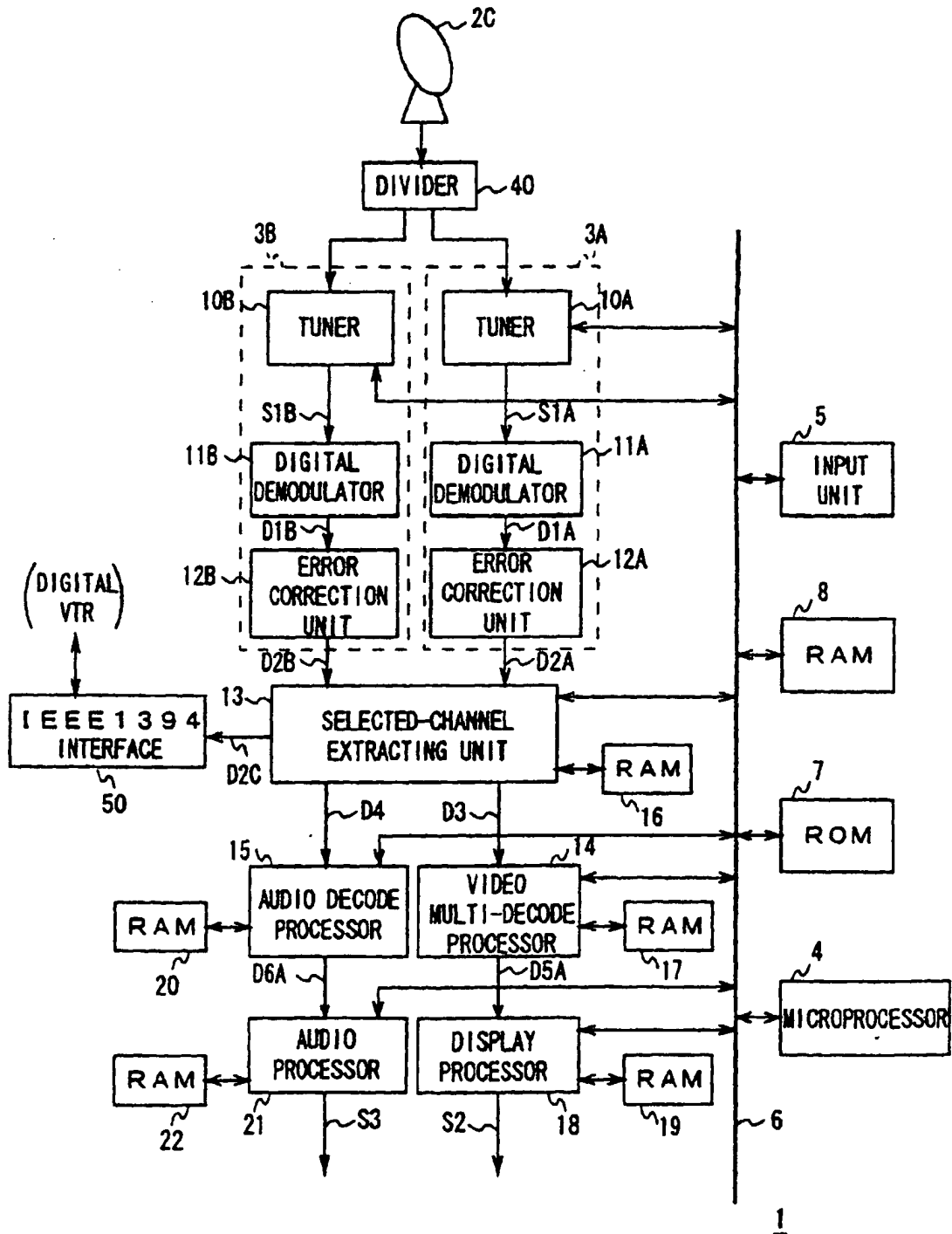


FIG. 8

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/02654

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> Int.Cl. <sup>6</sup> H04J3/04, H04J3/00, H04L12/56  According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b> Minimum documentation searched (classification system followed by classification symbols) Int.Cl. <sup>6</sup> H04J3/00, H04L12/56, H04N7/08, H04N5/45  Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926-1999 Kokai Jitsuyo Shinan Koho 1971-1999  Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP, 9-186665, A (Victor Co. of Japan, Ltd.), 15 July, 1997 (15. 07. 97), Page 4, column 6, lines 8 to 20 ; Fig. 1 & EP, 774753, A2	1, 3, 4, 6 2, 5, 7-12
A		
X	JP, 9-307826, A (Matsushita Electric Industrial Co., Ltd.), 28 November, 1997 (28. 11. 97), Page 3, column 4, lines 2 to 22 ; Fig. 5 (Family: none)	1, 3, 4, 6, 7, 9, 10, 12 2, 5, 8, 11
A		
PX	JP, 10-271464, A (Victor Co. of Japan, Ltd.), 9 October, 1998 (09. 10. 98), Page 3, column 3, lines 5 to 10 (Family: none)	1, 3, 4, 6 2, 5, 7-12
PA		
A	JP, 9-162821, A (Toshiba Corp.), 20 June, 1997 (20. 06. 97) (Family: none)	1-12
A	JP, 5-328321, A (Toshiba Corp.), 10 December, 1993 (10. 12. 93) (Family: none)	1-12
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "Z" document member of the same patent family		
Date of the actual completion of the international search 12 August, 1999 (12. 08. 99)		Date of mailing of the international search report 24 August, 1999 (24. 08. 99)
Name and mailing address of the ISA/ Japanese Patent Office  Facsimile No.		Authorized officer  Telephone No.